CS406: Compilers Spring 2021

Week 12: Control Flow Graphs, Data Flow Analysis

Basic Blocks and Flow Graphs

Basic Block

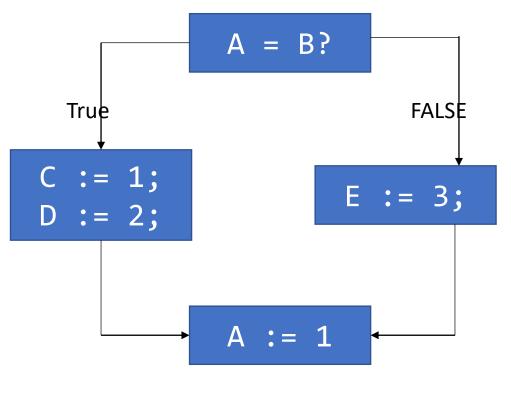
- Maximal sequence of consecutive instructions with the following properties:
 - The first instruction of the basic block is the *only entry point*
 - The last instruction of the basic block is either the halt instruction or the only exit point

Flow Graph

- Nodes are the basic blocks
- Directed edge indicates which block follows which block

Basic Blocks and Flow Graphs - Example

```
if A = B then
   C := 1;
   D := 2;
else
   E := 3
fi
A := 1;
```



A data flow graph

Flow Graphs

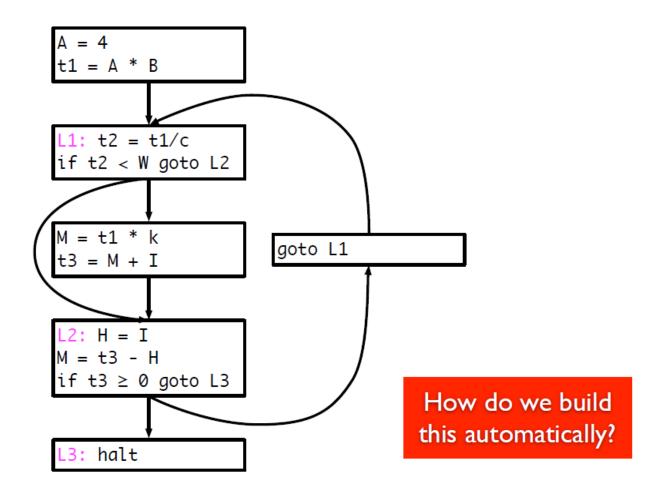
- Capture how control transfers between basic blocks due to:
 - Conditional constructs
 - Loops
- Are necessary when we want optimize considering larger parts of the program
 - Multiple procedures
 - Whole program

Flow Graphs - Representation

- We need to label and track statements that are jump targets
 - Explicit targets targets mentioned in jump statement
 - Implicit targets targets that follow conditional jump statement
 - Statement that is executed if the branch is not taken
- Implementation
 - Linked lists for BBs
 - Graph data structures for flow graphs

```
A = 4
t1 = A * B
repeat {
   t2 = t1/C
   if (t2 ≥ W) {
      M = t1 * k
      t3 = M + I
   }
   H = I
   M = t3 - H
} until (T3 ≥ 0)
```

CFG for running example



Constructing a CFG

- To construct a CFG where each node is a basic block
 - Identify leaders: first statement of a basic block
 - In program order, construct a block by appending subsequent statements up to, but not including, the next leader
- Identifying leaders
 - First statement in the program
 - Explicit target of any conditional or unconditional branch
 - Implicit target of any branch

Partitioning algorithm

- Input: set of statements, stat(i) = ith statement in input
- Output: set of leaders, set of basic blocks where block(x) is the set of statements in the block with leader x
- Algorithm

```
A = 4
     t1 = A * B
  L1: t2 = t1 / C
4
     if t2 < W goto L2
5
        M = t1 * k
6
      t3 = M + I
  L2: H = I
8
        M = t3 - H
        if t3 \ge 0 goto L3
9
        goto L1
10
11 L3:
        halt
```

```
Leaders =
Basic blocks =
```

```
1          A = 4
2          t1 = A * B
3          L1:     t2 = t1 / C
4          if t2 < W goto L2
5          M = t1 * k
6          t3 = M + I
7          L2:     H = I
8          M = t3 - H
9          if t3 ≥ 0 goto L3
10          goto L1
11     L3:     halt</pre>
```

```
Leaders = \{1\}
Basic blocks =
```

```
1          A = 4
2          t1 = A * B
3          L1:     t2 = t1 / C
4          if t2 < W goto L2
5          M = t1 * k
6          t3 = M + I
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8          M = t3 - H
9          if t3 ≥ 0 goto L3
10          goto L1
11     L3: halt</pre>
```

Leaders = $\{1,3\}$ Basic blocks =

```
1          A = 4
2          t1 = A * B
3          L1: t2 = t1 / C
4          if t2 < W goto L2
5          M = t1 * k
6          t3 = M + I
7          L2: H = I
8          M = t3 - H
9          if t3 ≥ 0 goto L3
10          goto L1
11     L3: halt</pre>
```

Leaders = $\{1,3\}$ Basic blocks =

```
A = 4
t1 = A * B
3 L1: t2 = t1 / C
  if t2 < W goto L2
       M = t1 * k
5
6
     t3 = M + I
  L2: H = I
    M = t3 - H
8
9
  if t3 ≥ 0 goto L3
  goto L1
10
11 L3: halt
```

Leaders = $\{1,3,5\}$ Basic blocks =

```
1          A = 4
2          t1 = A * B
3          L1:     t2 = t1 / C
4          if t2 < W goto L2
5          M = t1 * k
6          t3 = M + I
7          L2:     H = I
8          M = t3 - H
9          if t3 ≥ 0 goto L3
10          goto L1
11     L3:     halt</pre>
```

Leaders = $\{1,3,5\}$ Basic blocks =

```
1          A = 4
2          t1 = A * B
3          L1:     t2 = t1 / C
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10          goto L1
11     L3:     halt</pre>
```

Leaders =
$$\{1,3,5,7\}$$

Basic blocks =

```
1          A = 4
2          t1 = A * B
3          L1:     t2 = t1 / C
4          if t2 < W goto L2
5          M = t1 * k
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Leaders =
$$\{1,3,5,7\}$$

Basic blocks =

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1          A = 4
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6          t3 = M + I
7          L2: H = I
8          M = t3 - H
9          if t3 ≥ 0 goto L3
10          goto L1
11     L3: halt</pre>
```

Leaders = $\{1,3,5,7\}$ Basic blocks =

```
1          A = 4
2          t1 = A * B
3          L1:     t2 = t1 / C
4          if t2 < W goto L2
5          M = t1 * k
6          t3 = M + I
7          L2:     H = I
8          M = t3 - H
9          if t3 ≥ 0 goto L3
10          goto L1
11     L3:     halt</pre>
```

Leaders = $\{1,3,5,7,10\}$ Basic blocks =

```
A = 4
t1 = A * B
3 L1: t2 = t1 / C
4 if t2 < W goto L2
5
  M = t1 * k
  t3 = M + I
7 L2: H = I
8
     M = t3 - H
       if t3 \ge 0 goto L3
10
       goto L1
11
  L3:
       halt
```

```
Leaders = {1,3,5,7,10,11}
Basic blocks =
```

```
A = 4
t1 = A * B
3 L1: t2 = t1 / C
4 if t2 < W goto L2
5
 M = t1 * k
6 	 t3 = M + I
7 L2: H = I
8
    M = t3 - H
    if t3 ≥ 0 goto L3
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  L3:
       halt
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```
Leaders = \{1,3,5,7,10,11\} Block\{1\} = ?
```

```
1          A = 4
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```

```
Leaders = \{1,3,5,7,10,11\} Block(1) = ?

Start from statement 2 and add till either the end or a leader is reached
```

```
A = 4
t1 = A * B
3 L1: t2 = t1 / C
4 if t2 < W goto L2
5
 M = t1 * k
6 	 t3 = M + I
7 L2: H = I
8
  M = t3 - H
    if t3 ≥ 0 goto L3
10
       goto L1
11
  L3:
       halt
```

```
Leaders = \{1,3,5,7,10,11\} Block\{1\} = \{1,2\} Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(3) = ? Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(3) = \{3,4\} Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(5) = ? Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(5) = \{5,6\} Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(7) = ? Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(7) = \{7,8,9\} Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(10) = ? Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block(10) = \{10\} Basic blocks =
```

```
Leaders = \{1,3,5,7,10,11\} Block\{11\} = \{11\} Basic blocks =
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```
1          A = 4
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9          if t3 ≥ 0 goto L3
10          goto L1
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```

```
Leaders = \{1, 3, 5, 7, 10, 11\}
Basic blocks = \{\{1, 2\}, \{3, 4\}, \{5, 6\}, \{7, 8, 9\}, \{10\}, \{11\}\}
```

Putting edges in CFG

- There is a directed edge from B₁ to B₂ if
 - There is a branch from the last statement of B_1 to the first statement (leader) of B_2
 - B₂ immediately follows B₁ in program order and B₁ does not end with an unconditional branch
- Input: block, a sequence of basic blocks
- Output:The CFG

```
for i = | to |block| {{1,2},{3,4},{5,6},{7,8,9},{10},{11}}}
  x = last statement of block(i)
  if stat(x) is a branch, then
    for each explicit target y of stat(x)
        create edge from block i to block y
    end for
  if stat(x) is not unconditional then
    create edge from block i to block i+1
end for
```

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 - There is a branch from the last statement of B_1 to the first statement (leader) of B_2
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- Input: block, a sequence of basic blocks

```
Output: The CFG
for i = I to |block| {{1,2},{3,4},{5,6},{7,8,9},{10},{11}}
x = last statement of block(i)
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- Input: block, a sequence of basic blocks

```
    Output: The CFG
    for i = 1 to |block| {{1,2},{3,4},{5,6},{7,8,9},{10},{11}}
    x = last statement of block(i)
    if stat(x) is a branch, then
    for each explicit target y of stat(x)
    create edge from block i to block y
    end for
    if stat(x) is not unconditional then
    create edge from block i to block i+1
    end for
```

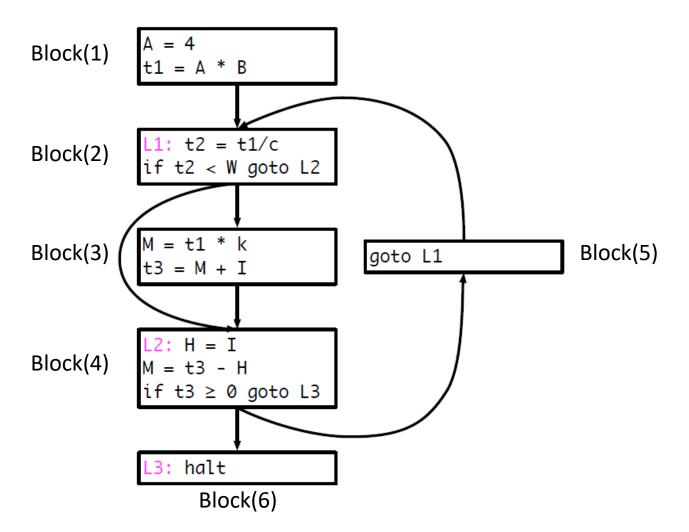
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```

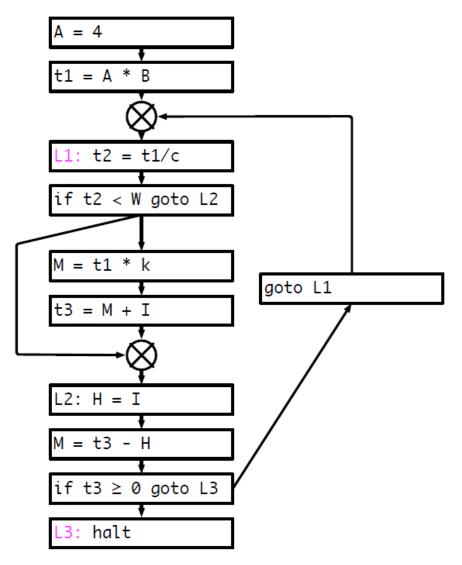
Result



Discussion

- Some times we will also consider the <u>statement-level</u> CFG, where each node is a statement rather than a basic block
 - Either kind of graph is referred to as a CFG
- In statement-level CFG, we often use a node to explicitly represent merging of control
 - Control merges when two different CFG nodes point to the same node
- Note: if input language is structured, front-end can generate basic block directly
 - "GOTO considered harmful"

Statement level CFG



Control Flow Graphs - Use

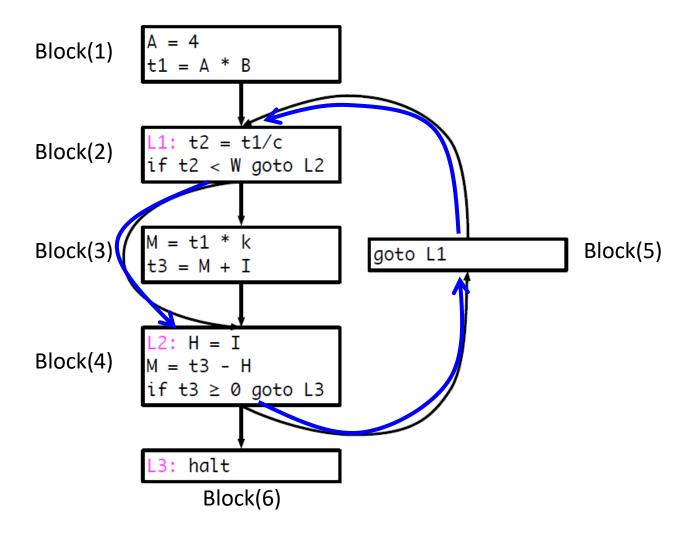
- Why do we need CFGs? Global Optimization
 - Optimizing compilers do global optimization (i.e. optimize beyond basic blocks)
 - Differentiating aspect of normal and optimizing compilers
 - E.g. loops are the most frequent targets of global optimization (because they are often the "hot-spots" during program execution)

how do we identify loops in CFGs?

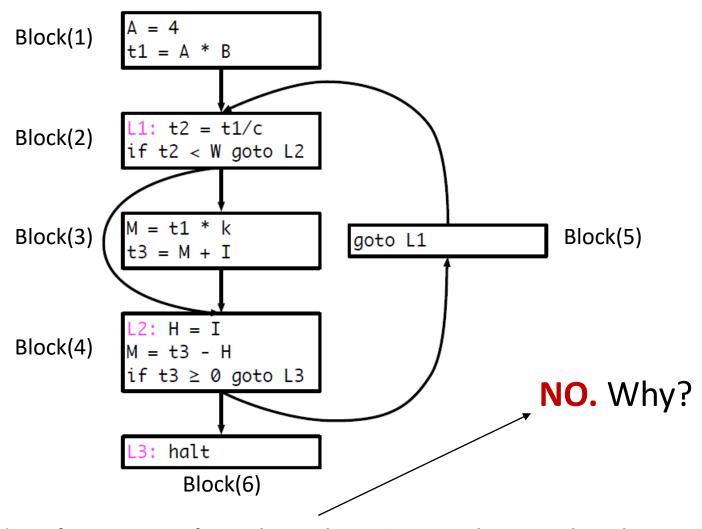
Loops – how do we identify loops in CFGs?

For a set of nodes, L, that belong to loop:

- 1) There is a *loop entry node* such that any path from the *graph entry node* to any node in L goes through the *loop entry node*. i.e. no node in L has a predecessor that is outside L.
- 2) Every node in L has a non-empty path, completely within L, to the entry of L.

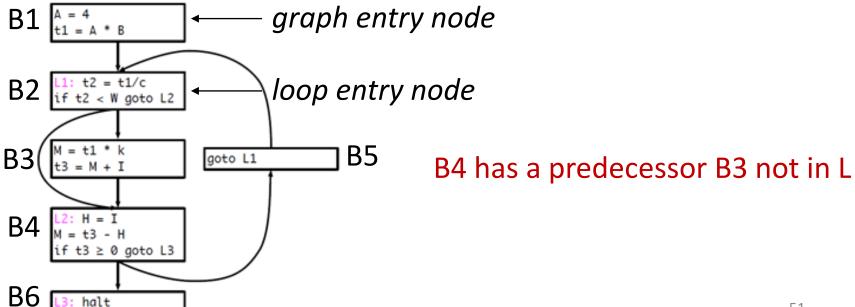


Consider: {B2, B4, B5}. Is this a loop?, Are there other loops?

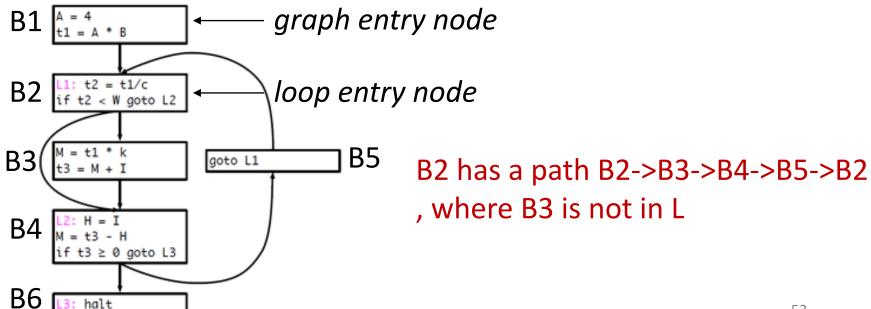


Consider: {B2, B4, B5}. Is this a loop?, Are there other loops?

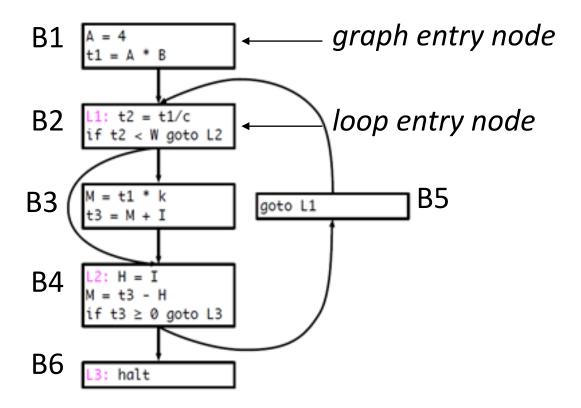
- 1) Is L={B2, B4, B5} a loop?. No. Consider:
 - There is a loop entry node such that any path from the graph entry node to any node in L goes through the loop entry node.
 i.e. no node in L has a predecessor that is outside L.



- 1) Is L={B2, B4, B5} a loop?. No. Consider:
 - Every node in L has a non-empty path, completely within L, to the entry of L.



1) Is L={B2, B3, B4, B5} a loop?.



Optimize Loops

Example - Code Motion
 Should be careful while doing optimization of loops

```
while J > I loop
    A(j) := 10/I;
    j := j + 2;
end loop;
```

Optimize Loops – Code Motion

 Should be careful while doing optimization of loops

```
while J > I loop
    A(j) := 10/I;
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Optimization: can move 10/I out of loop.

Optimize Loops – Code Motion

 Should be careful while doing optimization of loops

```
while J > I loop
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- Optimization: can move 10/I out of loop
- What if I = 0?

Optimize Loops – Code Motion

 Should be careful while doing optimization of loops

```
while J > I loop
    A(j) := 10/I;
    j := j + 2;
end loop;
```

- Optimization: can move 10/I out of loop
- What if I = 0?
- What if I != 0 but loop executes zero times?

Optimization Criteria - Safety and Profitability

- Safety is the code produced after optimization producing same result?
- Profitability is the code produced after optimization running faster or uses less memory or triggers lesser number of page faults etc.

```
while J > I loop
    A(j) := 10/I;
    j := j + 2;
end loop;
```

- E.g. moving I out of the loop introduces exception (when I=0)
- E.g. if the loop is executed zero times, moving I out is not profitable

- How do we identify expressions that can be moved out of the loop?
 - LoopDef = {} set of variables <u>defined</u> i.e. whose values are overwritten) in the loop body
 - LoopUse = { } 'relevant' variables <u>used</u> in computing an expression

```
Mark_Invariants(Loop L) {
```

- 1. Compute LoopDef for L
- Mark as invariant all expressions, whose relevant variables don't belong to LoopDef

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• Example

Invariant Expressions

```
For an array access, A[m] => Addr(A) + m
For 3D array above*, Addr(A[I][J][K]) =
Addr(A)+(I*10000)-10000+(J*100)-100+K-1
```

^{*}Assuming row-major ordering of storage

```
For an array access, A[m] => Addr(A) + m
For 3D array above*, Addr(A[I][J][K]) =
Addr(A)+(I*10000)-10000+(J*100)-100+K-1
```

Move the invariant expressions identified

```
Factor_Invariants(Loop L) {
  Mark Invariants(L);
  foreach expression E marked an invariant:
      1. Create a temporary T
      2. Replace each occurrence of E in L with T
      3. Insert T:=E in L's header code
         immediately after the first loop-
         termination test (i.e. after "j<!op>OUT" in slide 39,
         week9.pdf)
        //If loop is known to execute at least once,
         insert T:=E before LOOP:
```

Example

Expressions cannot always be moved out!

Case I: We can move t = a op b if the statement dominates all loop exits where t is live

A node a dominates node b if all paths to b must go through a

```
for (...) {
    if(*)
    a = 100
}
c=a
```

Cannot move a=100 because it does not dominate c=a i.e. there is one path (when if condition is false) c=a can be reached without going a=100

Expressions cannot always be moved out!

Case II: We can move t = a op b if there is only definition of t in the loop

```
for (...) {
   if(*)
      a = 100
   else
      a = 200
}
```

Multiple definition of a

• Expressions cannot always be moved out!

Case III: We can move t = a op b if t is not defined before the loop, where the definition reaches t's use after the loop

```
a=5
for (...) {
    a = 4+b
}
c=a
```

Definition of a in a=5 reaches c=a, which is defined after the loop

Optimize Loops –Strength Reduction

- Like strength reduction in peephole optimization
 - E.g. replace a*2 with a<<1
- Applies to uses of induction variable in loops
 - Basic induction variable (I) only definition within the loop is of the form I = I ± S, (S is loop invariant)
 - I usually determines number of iterations
 - Mutual induction variable (J) defined within the loop, its value is linear function of other induction variable, I, such that
 - J = I * C ± D (C, D are loop invariants)

Optimize Loops –Strength Reduction

```
strength_reduce(Loop L) {
  Mark Invariants(L);
   foreach expression E of the form I*C+D where I is
L's loop index and C and D are loop invariants
      1. Create a temporary T
      2. Replace each occurrence of E in L with T
      3. Insert T:=I_0*C+D, where I_0 is the initial value of the
         induction variable, immediately before L
      4. Insert T:=T+S*C, where S is the step size, at the end of
         L's body
```

Optimize Loops –Strength Reduction

- Suppose induction variable I takes on values $I_{o,j}$ $I_{o}+S$, $I_{o}+2S$, $I_{o}+3S$... in iterations 1, 2, 3, 4, and so on...
- Then, in consecutive iterations, Expression
 I*C+D takes on values

$$I_o*C+D$$

 $(I_o+S)*C+D = I_o*C+S*C+D$
 $(I_o+2S)*C+D = I_o*C+2S*C+D$

- The expression changes by a constant S*C
- Therefore, we have replaced a * and + with a +

Optimize Loops – Strength Reduction

Example (Applying to innermost loop)

```
for I = 1 to 100
                             for I=1 to 100
  for J = 1 to 100
                               temp3=Addr(A[i])
     for K = 1 to 100
                               for J=1 to 100
        A[I][J][K] = (I*J)*K
                                  temp1=Addr(temp3(J))
                                  temp2=I*J
                                  for K=1 to 100
                                     temp1[K]=temp2*K
                  temp2=I*J
                  temp4=temp2
                  for K=1 to 100
      //S=1
                     temp1[K]=temp4
      //C=temp2
                     temp4=temp4+temp2
                                                   71
```

Optimize Loops – Strength Reduction

Exercise (Apply to intermediate loop)

```
for I=1 to 100
                            temp2=I*J
  temp3=Addr(A[i])
                            temp4=temp2
  for J=1 to 100
                            for K=1 to 100
     temp1=Addr(temp3(J))
                               temp1[K]=temp4
     temp2=I*J
                               temp4=temp4+temp2
     for K=1 to 100
        temp1[K]=temp2*K
               // Induction var = J
               // S = 1
               // Expression = I * J
```

Optimize Loops – Strength Reduction

Exercise (Apply to intermediate loop)

```
temp5=I
for J=1 to 100
     temp1=Addr(temp3(J))
     temp2=temp5
     temp4=temp2
     for K=1 to 100
        temp1[K]=temp4
        temp4=temp4+temp2
     temp5=temp5+I
```

Optimize Loops – Strength Reduction

Further strength reduction possible?

```
for I=1 to 100
  temp3=Addr(A[i])
  temp5=I
  for J=1 to 100
     temp1=Addr(temp3(J))
     temp2=temp5
     temp4=temp2
     for K=1 to 100
        temp1[K]=temp4
        temp4=temp4+temp2
     temp5=temp5+I
```

Optimize Loops – Loop Unrolling

- Modifying induction variable in each iteration can be expensive
- Can instead unroll loops and perform multiple iterations for each increment of the induction variable
- What are the advantages and disadvantages?

```
for (i = 0; i < N; i++)
    A[i] = ...

    Unroll by factor of 4

for (i = 0; i < N; i += 4)
    A[i] = ...
    A[i+1] = ...
    A[i+2] = ...
</pre>
```

 $A[i+3] = \dots$

Optimize Loops - Summary

- Low level optimization
 - Moving code around in a single loop
 - Examples: loop invariant code motion, strength reduction, loop unrolling
- High level optimization
 - Restructuring loops, often affects multiple loops
 - Examples: loop fusion, loop interchange, loop tiling

Useful optimizations

- Common subexpression elimination (global)
 - Need to know which expressions are available at a point
- Dead code elimination
 - Need to know if the effects of a piece of code are never needed, or if code cannot be reached
- Constant folding
 - Need to know if variable has a constant value
- So how do we get this information?

Dataflow analysis

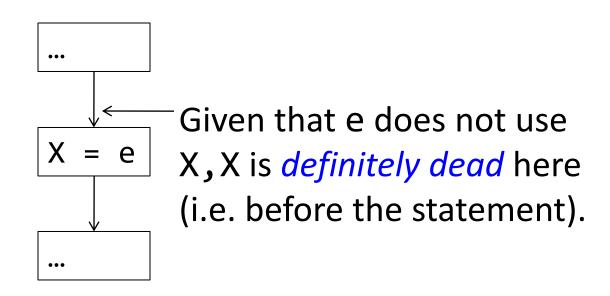
- Framework for doing compiler analyses to drive optimization
- Works across basic blocks
- Examples
 - Constant propagation: determine which variables are constant
 - Liveness analysis: determine which variables are live
 - Available expressions: determine which expressions are have valid computed values
 - Reaching definitions: determine which definitions could "reach" a use

Liveness – Recap...

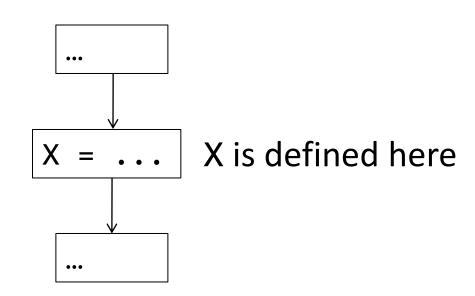
- A variable X is live at statement S if:
 - There is a statement S' that uses X
 - There is a path from S to S'
 - There are no intervening definitions of X

Liveness – Recap...

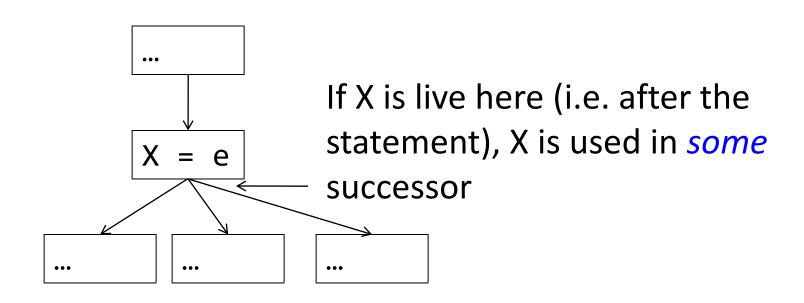
- A variable X is dead at statement S if it is not live at S
 - What about \dots ; X = X + 1?



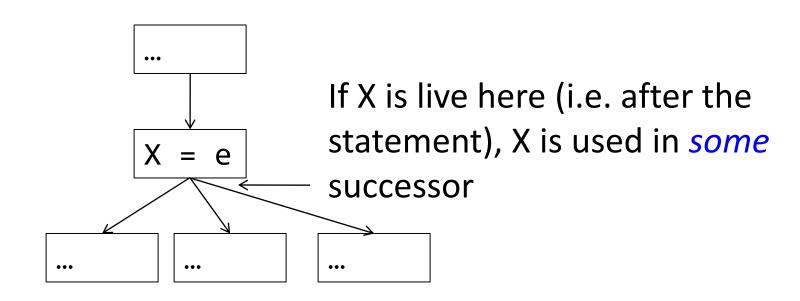
• Define a set LiveIn(b), where b is a basic block, as: the set of all variables live at the entrance of a basic block



Define a set Def(b), where b is a basic block,
 as: the set of all variables that are defined within block b

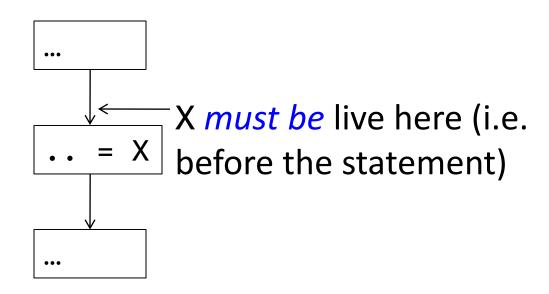


• Define a set LiveOut(b), where b is a basic block, as: the set of all variables live at the exit of a basic block



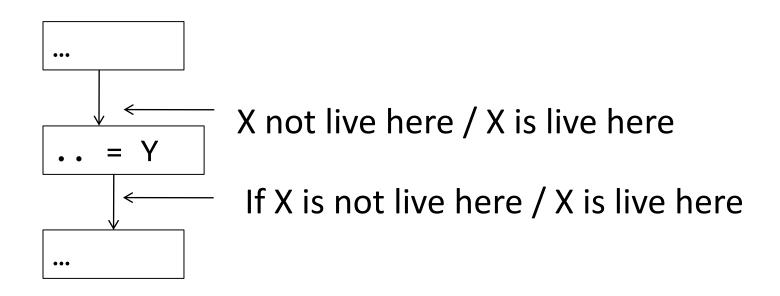
• If S(b) is the set of all successors of b, then

LiveOut(b) =
$$\bigcup_{i \in S(b)}$$
 LiveIn(i)



 Define a set LiveUse(b), where b is a basic block, as: the set of all variables that are used within block b. LiveIn(b) ⊇ LiveUse(b)

Liveness in a CFG - Observation



•If a node neither uses nor defines X, the liveness property remains the same before and after executing the node

• If a variable is live on exit from b, it is either defined in b or live on entrance to b

•Under what scenarios can a variable be live at the entrance of a basic block?

• If a variable is live on exit from b, it is either defined in b or live on entrance to b

- •Under what scenarios can a variable be live at the entrance of a basic block?
 - •Either the variable is used in the basic block

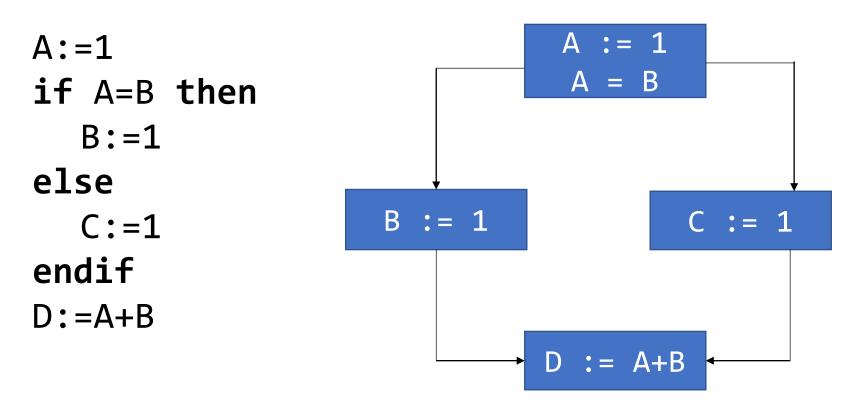
• If a variable is live on exit from b, it is either defined in b or live on entrance to b

- •Under what scenarios can a variable be live at the entrance of a basic block?
 - Either the variable is used in the basic block
 - •OR the variable is live at exit and not defined within the block

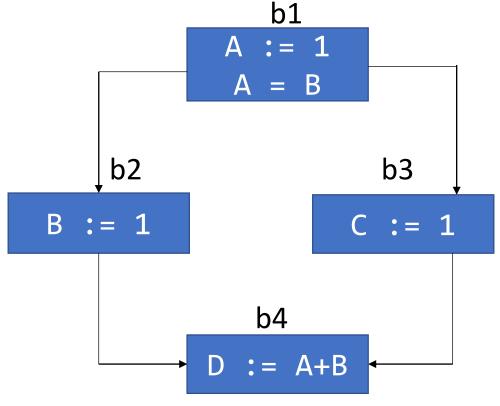
- Under what scenarios can a variable be live at the entrance of a basic block?
 - Either the variable is used in the basic block
 - •OR the variable is live at exit and not defined within the block

```
LiveIn(b) = LiveUse(b) U (LiveOut(b) -
Def(b))
```

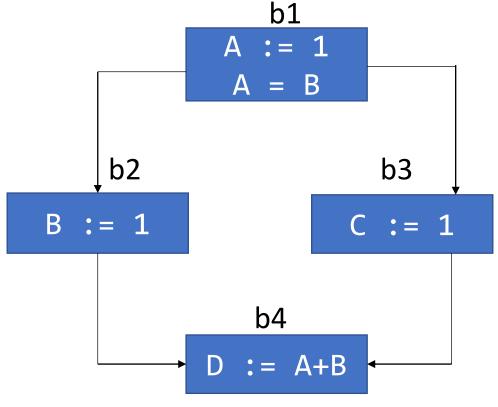
Draw CFG for the code:



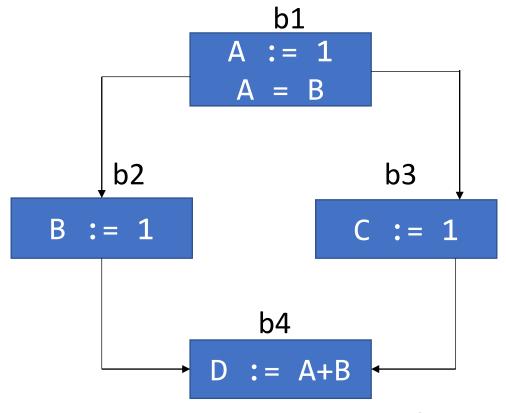
Block	Def	LiveUse
b1		
b2		
b3		
b4		



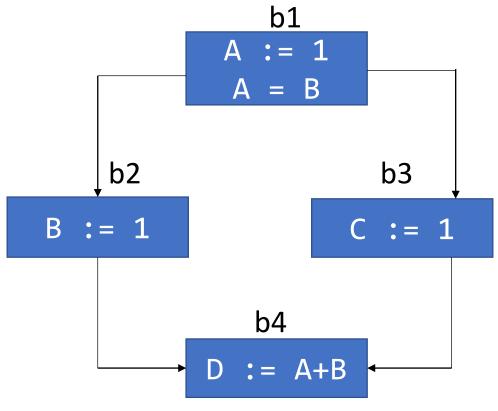
Block	Def	LiveUse
b1	{A}	{B}
b2		
b3		
b4		



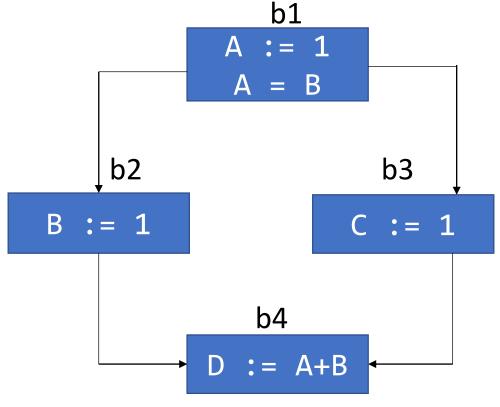
Block	Def	LiveUse
b1	{A}	{B}
b2	{B}	{}
b3		
b4		



Block	Def	LiveUse
b1	{A}	{B}
b2	{B}	{}
b3	{C}	{}
b4		



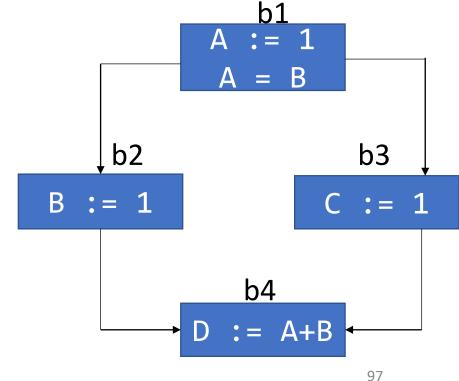
Block	Def	LiveUse
b1	{A}	{B}
b2	{B}	{}
b3	{C}	{}
b4	{D}	{A,B}



start from use of a variable to its definition.

Is this analysis going backward or forward w.r.t. control flow?

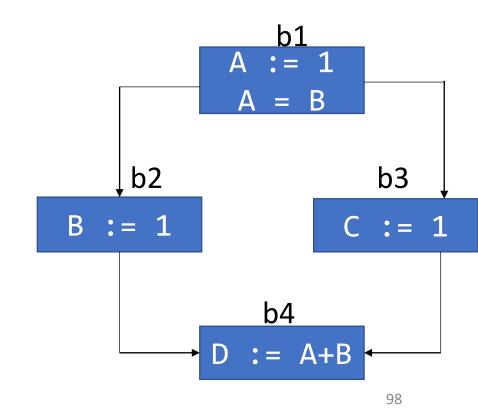
Block	Def	LiveUse
b1	{A}	{B}
b2	{B}	{}
b3	{C}	{}
b4	{D}	{A,B}



start from use of a variable to its definition.

Backward-flow problem

Block	Def	LiveUse
b1	{A}	{B}
b2	{B}	{}
b3	{C}	{}
b4	{D}	{A,B}



- Start from use of a variable to its definition.
- •Compute:

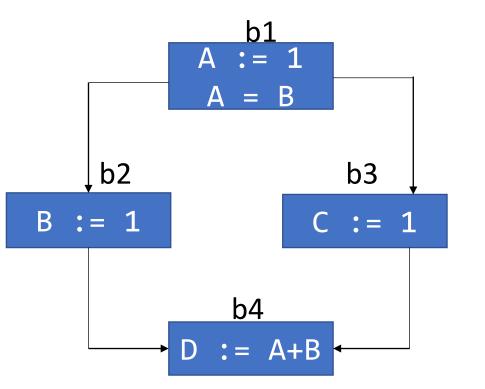
Block	Def	LiveUse
b1	{A}	{B}
b2	{B}	{}
b3	{C}	{}
b4	{D}	{A,B}

- Start from use of a variable to its definition.
- •Compute:

Block	Def	LiveUse
b1	{A}	{B}
b2	{B}	{}
b3	{C}	{}
b4	{D}	{A,B}

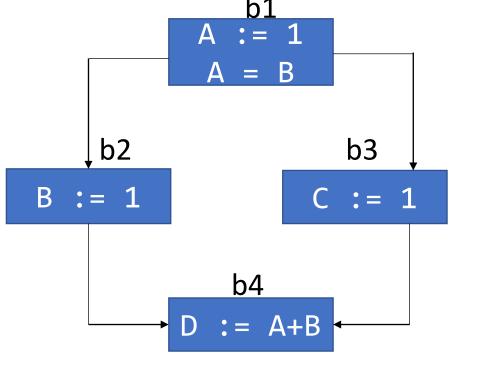
Block	Liveln	LiveOut
b1	{B}	{A,B}
b2	{A}	{A,B}
b3	{A,B}	{A,B}
b4	{A,B}	{}

- Assume that the CFG below represents your entire program
 - •What can you infer from the table?



Block	Liveln	LiveOut
b1	{B}	{A,B}
b2	{A}	{A,B}
b3	{A,B}	{A,B}
b4	{A,B}	{}

- Assume that the CFG below represents your entire program
 - •Variable B is live in b1, the entry basic block of CFG. This implies that B is used before it is defined. An error!



Block	Liveln	LiveOut
b1	{B}	{A,B}
b2	{A}	{A,B}
b3	{A,B}	{A,B}
b4	{A,B}	{}